

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Knorr, *et al.*

Attorney Docket: 2003 P 54098 US

Filed: Herewith

Examiner: TBD

Serial No.: TBD

Art Unit: TBD

For: Sidewall Sealing of Porous Dielectric Materials

Mail Stop: Patent Application
Commissioner for Patents
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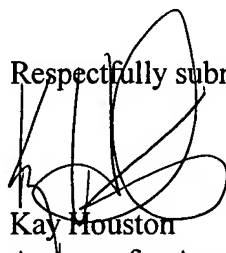
INFORMATION DISCLOSURE STATEMENT

Dear Sir:

The Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

December 11, 2003
Date

Respectfully submitted,

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Compleat if Known	
				Application Number	TBD
				Filing Date	HEREWITH
				First Named Inventor	Knorr, <i>et al.</i>
				Art Unit	TBD
Examiner Name	TBD				
Sheet	1	of	1	Attorney Docket Number	2003 P 54098 US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-			
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FOREIGN PATENT DOCUMENTS						
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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	1	CALUWAERTS, R., <i>et al.</i> , Post Patterning Meso Porosity Creation: A Potential Solution for Pore Sealing, International Interconnect Technology Conference, June 2-4, 2003, pp. 242-244, Omnipress, Madison, WI.	
	2	HU, B.K. <i>et al.</i> , "Spin-On Barrier for Pore-Sealing on 2.2 Porous Ultra Low-K Material", Advanced Metallization Conference (AMC) 2003, Oct. 21 - 23, 2003, Montreal, Canada.	
	3	MOURIER, T., <i>et al.</i> , Porous low k pore sealing process study for 65 nm and below technologies, International Interconnect Technology Conference, June 2-4, 2003, pp. 245-247, Omnipress, Madison, WI.	
	4	PARK, JAE-EUN <i>et al.</i> , Mass-Productive Ultra-Low Temperature ALD SiO ₂ Process Promising for Sub-90nm Memory and Logic Devices, International Electron Devices Meeting Technical Digest, December 8-11, 2002, San Francisco, CA.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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